

Proceedings of the First SPIN Workshop

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Featured presentations

- H. Erdogmus**, National Research Council of Canada, *Verifying Semantic Relations in SPIN**.
- D. Peled, G. Holzmann**, AT&T Bell Laboratories, *Checking Linear Temporal Logic Properties*.
- D. Peled, G. Holzmann**, AT&T Bell Laboratories, *Partial Order Reduction of the State Space*.
- P. Godefroid**, AT&T Bell Laboratories, *The ULg Partial Order Package for SPIN*.
- J.-Ch. Grégoire**, INRS-Télécommunications, *Issues in State Space Reduction*.
- A. Joesang**, Norwegian Institute of Technology, *Security Protocol Verification using SPIN*.
- S. Leue**, University of Waterloo, *Implementing MSCs in PROMELA*.
- E. Najm, F. Olsen**, ENST, *Reactive spin and PROMELA*.
- M. G. Staskauskas**, AT&T Bell Laboratories, *Industrial Experience with Formal Validation*.
- T. Cattel**, Ecole Polytechnique Fédérale de Lausanne, *Process Control Design Using SPIN*.
- G. Duval, J. Julliand**, Ecole Polytechnique Fédérale de Lausanne, *Modeling and Verification of the RUBIS μ -Kernel with SPIN*.
- P. Kars**, University of Twente, *Design of a Storm Surge Barrier control System*.
- F. J. Lin**, Bellcore, *Two applications of PROMELA/spin*.
- B. Rahardjo**, U. of Manitoba, *spin as a Hardware Design Tool*.

Complementary material

M. Daniele, P. Reditore, R. Manione, CSELT, *Interactive Time Simulation of Distributed Systems*.

S. Tripakis, C. Coucourbetis, University of Crete, *Extending PROMELA and Spin for Real-Time*.